



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
 Shunpei YAMAZAKI et al.)
 Serial No. 09/916,484) Art Unit: Unknown
 Filed: July 30, 2001) Examiner: Unknown
 For: ELECTRIC DEVICE, MATRIX DEVICE,)
 ELECTRO-OPTICAL DISPLAY DEVICE,)
 AND SEMICONDUCTOR MEMORY)
 HAVING THIN-FILM TRANSISTORS)

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CERTIFICATE OF MAILING
 I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on October 17, 2001.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents and Trademarks
 Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

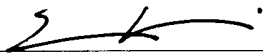
The references listed on the attached Form PTO-1449 were previously cited in an Information Disclosure Statement dated July 30, 2001. Due to several typographical errors, it is respectfully requested that the Examiner line through the below listed Japanese references, in favor of the references listed on the attached Form PTO-1449.

0194351	0154383	0011329	0113971
0269358	0282464	0154383	0099778
0159544	0159826	0089030	0096636
0144297	0081621	0144297	1-144297
04-151820	04-155960	62-120354	

U.S. Patent Nos. 4,783,145; 5,598,106 and 5,998,841 are being cited for the first time in the subject application, however, they were cited in parent application Serial No. 09/375,352 filed August 17, 1999, and/or the predecessor applications including Serial Nos. 08/684,394; 08/268,466; 08/413,885 and 08/068,672.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380 (740756-2344).

Respectfully submitted,



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